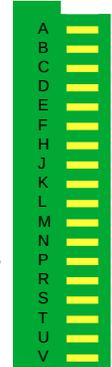


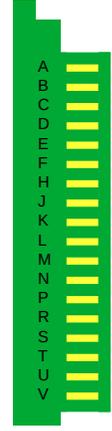
## DEC PDP11, CPU J11 "JAWS" PINOUT

/TEST →	↔ DAL6
AI00 ←	↔ DAL7
AI01 ←	↔ DAL8
AI02 ←	↔ DAL0
AI03 ←	↔ DAL9
/PWRF →	↔ DAL10
/FPE →	↔ DAL11
/EVENT →	↔ DAL12
HALT →	↔ DAL13
IRQ0 →	↔ DAL14
IRQ1 →	↔ DAL15
IRQ2 →	↔ DAL1
IRQ3 →	↔ DAL2
/PARITY →	↔ DAL3
GND	Vcc
Vcc	GND
BS0 ←	↔ DAL4
BS1 ←	↔ DAL5
/MAP ←	← DV
/ABORT ↔	→ /BUFCTL
DAL21 ←	→ /ALE
DAL20 ←	→ /STRB
DAL19 ←	→ /SCTL
DAL18 ←	→ XTAL0
DAL17 ←	← XTAL1
DAL16 ←	↔ CLK
/DMR →	→ CLK2
/MISS →	← /INIT
/PRDC ←	← /CONT
NC	← /TEST2

SLOT/FIN	PIN	PIN#	SIDE	NAME			CPU M	CPU D	DBK-2	COMP?	PATS	DESINYS
A	A	1	1	BIRQ5	spare	BIRQ5						
A	B	2	1	BIRQ6	spare	BIRQ6						
A	C	3	1	BDAL16	spare	BDAL16						
A	D	4	1	BDAL17		BDAL17						
A	E	5	1	SINGLE STEP		SINGLE STEP		NC				
A	F	6	1	SRUN		SRUN						
A	H	7	1	SRUN		SRUN		NC				
A	J	8	1	GND								
A	K	9	1	REF KILL			NC	NC				
A	L	10	1	?			NC	NC				
A	M	11	1	GND								
A	N	12	1	BDMR	BDMR	BDMR						
A	P	13	1	HALT	HALT	HALT				DESINE	ZIURINT	IS VIRSAUS
A	R	14	1	BREF		BREF						
A	S	15	1	+12VBBU			NC	NC				
A	T	16	1	GND								
A	U	17	1	?			NC	NC				
A	V	18	1	+5VBBU								
<hr/>												
A	A	1	2	+5V						SOLDER?	PATS	DESINYS
A	B	2	2	?			NC	NC				
A	C	3	2	GND								
A	D	4	2	+12V	+12V			NC				
A	E	5	2	BDOUT		BDOUT						
A	F	6	2	BRPLY		BRPLY						
A	H	7	2	BDIN		BDIN						
A	J	8	2	BSYNC		BSYNC						
A	K	9	2	BWTB		BWTB						
A	L	10	2	BIRQ4		BIRQ4						
A	M	11	2	BUS GRANT AN2		MMUSTR						
A	N	12	2	BUS GRANT AM2	BIAKO	BIAK						
A	P	13	2	BBS7	bank select	BBS7						
A	R	14	2	BUS GRANT AS2		BUBMAP						
A	S	15	2	BUS GRANT AR2	BDMSO	BDMGO						
A	T	16	2	BINIT		BINIT			CBPOC			
A	U	17	2	BDAL0	BDAL0	BDAL0						
A	V	18	2	BDAL1		BDAL1						



B	A	1	1	BDCOK		BDCOK						
B	B	2	1	BPOK	BPOKH	BPOK						
B	C	3	1	BDAL18	BDAL18	BDAL18						
B	D	4	1	BDAL19	BDAL19	BDAL19						
B	E	5	1	BDAL20	BDAL20	BDAL20						
B	F	6	1	BDAL21	BDAL21	BDAL21						
B	H	7	1	CLK DIS L'		CLK DIS L'						
B	J	8	1	GND								
B	K	9	1	-5V				NC				
B	L	10	1	-5V				NC				
B	M	11	1	GND								
B	N	12	1	BSACK	BSACK	BSACK						
B	P	13	1	BIRQ7	spare	BIRQ7						
B	R	14	1	BEVENT	BEVENT	BEVENT						
B	S	15	1	?				NC				
B	T	16	1	GND								
B	U	17	1	?				NC				
B	V	18	1	+5V								
B	A	1	2	+5V	+5V							
B	B	2	2	?			NC	NC				
B	C	3	2	GND								
B	D	4	2	+12V	+12V			NC				
B	E	5	2	BDAL2	BDAL2			2				
B	F	6	2	BDAL3	BDAL3			3				
B	H	7	2	BDAL4				4				



B	J	8	2	BDAL5	5
B	K	9	2	BDAL6	6
B	L	10	2	BDAL7	7
B	M	11	2	BDAL8	8
B	N	12	2	BDAL9	9
B	P	13	2	BDAL10	10
B	R	14	2	BDAL11	11
B	S	15	2	BDAL12	BDAL12
B	T	16	2	BDAL13	BDAL13
B	U	17	2	BDAL14	BDAL14
B	V	18	2	BDAL15	BDAL15

